

REMARKS

This responds to the Office Action mailed on November 1, 2007.

Claims 1 and 30 are amended, and no claims are canceled or added; as a result, claims 1-3, 7, 8, 10-12, and 30-33 are now pending in this application.

35 U.S.C. §112 Rejection of the Claims

Claims 1-3, 7-8, 10-12 and 30-33 were rejected under 35 U.S.C. § 112, first paragraph, as failing to comply with the enablement requirement. Applicants have amended independent claims 1 and 30 to overcome this rejection. Claim 1 has been amended to recite “a power management control block to control an operating frequency of the microprocessor in response to the temperature”. Claim 30 has been amended to recite “a power management control block to control a voltage provided to the integrated circuit in response to the temperature”. Applicants respectfully submit that these limitations are described sufficiently to enable one skilled in the art to practice the invention. See, for example, 410, Figure 4, and page 7 of the application as filed.

Claims 1-3, 7-8, 10-12 were rejected under 35 U.S.C. § 112, first paragraph, as failing to comply with the written description requirement. Claim 1 has been amended to overcome this rejection. Specifically, claim 1 has been amended to use the wording “operating frequency”. As noted by the examiner, this language is supported in the specification as filed.

35 U.S.C. §103 Rejection of the Claims

Claims 1-3, 7-8, 10-12 and 30-33 were rejected under 35 U.S.C. § 103(a) as being unpatentable over Fukazu et al. (U.S. 2002/0011327) or Gottmann et al. (U.S. 2003/0157386) in view of Margiott et al. (U.S. 6,519,510).

Independent claims 1 and 30 have been amended. Applicants respectfully submit that the amended portions of claims 1 and 30 recite structure and do not simply recite intended use. For example, claim 1 now includes the limitation “a power management control block to control an operating frequency of the microprocessor in response to the temperature”, and claim 30 now includes the limitation “a power management control block to control a voltage provided to the integrated circuit in response to the temperature”. Applicants further submit that the cited

references, taken alone or in any combination, do not disclose, teach, or suggest, the independent claims as amended. Specifically, applicants submit that the cited references do not disclose, teach, or suggest "a power management control block to control an operating frequency of the microprocessor in response to the temperature" or "a power management control block to control a voltage provided to the integrated circuit in response to the temperature".

CONCLUSION

Applicant respectfully submits that the claims are in condition for allowance and notification to that effect is earnestly requested. The Examiner is invited to telephone Applicant's attorney (952-473-8800) to facilitate prosecution of this application.

Respectfully submitted,

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By their Representatives,

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Date 1-2-08

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CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Mail Stop Amendment, Commissioner of Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 2 day of January, 2008.

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